

We claim:

1. A video signal timer switch, comprising, in combination:

a television monitor;

an incoming signal source;

a video switch interconnecting said television monitor
and said signal source, said switch comprising:

a housing comprising:

a top, a bottom and four sides interconnecting
said top and bottom;

a control pad within said housing and
protruding through said housing top, said
pad being comprised of a plurality of
keys adapted to enter codes and commands
into the video switch;

a plurality of video input jacks each adapted
to receive said incoming signal source;

a video output jack adapted to operatively
connect said signal source to said
television monitor;

an electronic control input module within said housing, said control module adapted to receive signals from said control pad, translate said signals into control signals and pass said control signals to a microprocessor within the housing;

a microprocessor within said housing and interconnected to said control module, wherein said microprocessor is adapted to receive said control signals from the control pad;

an input switch within said housing interconnected to said microprocessor, said input switch adapted to receive and select an incoming signal source; and

a video controller/combiner within said housing and interconnected to said microprocessor, said input switch, and said video output jack, said controller/combiner adapted to interconnect said incoming signal source with said television monitor.

2. A video signal timer switch as recited in claim 1,
further comprising:

programming means within said microprocessor wherein
said programming means allots an amount of time in
which said video controller/combiner interconnects
said incoming signal source with said television
monitor.

3. A video signal timer switch as recited in claim 2,
wherein:

 said programming means includes means for displaying
 visual indicia on said television monitor.

4. A video signal timer switch as recited in claim 3,
wherein:

 said video controller/combiner includes means for
 combining said visual indicia with said incoming
 signal source.

5. A video signal timer switch, comprising, in combination:

a television monitor;

an incoming signal source;

a video switch interconnecting said television monitor and said signal source, said switch comprising:

a first housing comprising:

a top, a bottom, and four sides

interconnecting said top and bottom;

a control pad within said first housing and protruding through said first housing top, said pad being comprised of a plurality of keys adapted to entering codes and commands into the video switch;

an infrared transmitter within said first housing, electronically connected to said control pad, and having means along one of said sides for transmitting an infrared signal;

a second housing comprising:

a top, a bottom, and four sides

interconnecting said top and bottom;

a plurality of video input jacks each of which is adapted to receive said incoming signal source;

a video output jack adapted to operatively connect said signal source to said television monitor;

an infrared receiver within said second housing, and having means along one of said sides for receiving an infrared signal;

an electronic control input module within said second housing and electronically connected to said infrared receiver, said control module adapted to receive signals from said control pad through said infrared transmitter and said infrared receiver, translate said signals into control signals and pass said control signals to a microprocessor within the second housing;

a microprocessor within said housing and interconnected to said control module, wherein said microprocessor is adapted to receive said control signals from said control module;

an input switch within said second housing and interconnected to said microprocessor, said input switch adapted to receive and select an incoming signal source; and

a video controller/combiner within said second housing and interconnected to said microprocessor, said input switch, and said video output jack, said controller/combiner adapted to interconnect said incoming signal source with said television monitor.

6. A video signal timer switch as recited in claim 5, further comprising:

programming means within said microprocessor wherein said programming means allots an amount of time in which said video controller/combiner interconnects said incoming signal source with said television monitor.

7. A video signal timer switch as recited in claim 6,
wherein:

 said programming means includes means for displaying
 visual indicia on said television monitor.

8. A video signal timer switch as recited in claim 7,
wherein:

 said video controller/combiner includes means for
 combining said visual indicia with said incoming
 signal source.